36. (Amended) A method for reducing voids in a metal material that has been electrolytically deposited into recessed microstructures defined in a surface of a microelectronic workpiece comprising:

electrolytically depositing a metal to substantially fill recessed sub-micron structures in the surface of the workpiece; and then

subjecting the workpiece to an annealing process at a temperature that is at or below about 250 degrees Celsius, the workpiece being subjected to a controlled temperature gradient in which the temperature decreases along a cross-section of the workpiece in a direction that is opposite to the direction of formation of the metal material during its deposition.

37. A method as set forth in Claim 36 wherein the metal material comprises copper.

38. (Canceled)

39. (Canceled)

40. A method for reducing voids in a metal material that has been electrolytically deposited into recessed microstructures defined on a surface of a microelectronic workpiece comprising:

electrolytically depositing a metal to substantially fill recessed sub-micron structures on the surface of the workpiece; and then

subjecting the workpiece to an annealing process to generate a controlled temperature gradient in which the temperature decreases along a cross-section of the workpiece in a direction that is toward the surface in which the recessed sub-micron structures are formed.

68-70. (Canceled)

71. The method of Claim 36, further comprising depositing a barrier layer on the surface of the workpiece in which the recessed microstructures are formed prior to depositing metal to substantially fill the recessed microstructures.

72. (Canceled)

73. The method of Claim 36, further comprising depositing a seed layer, substantially comprised of the same metal to be deposited to substantially fill the recessed microstructures, onto the surface of the workpiece prior to depositing the metal to substantially fill the recessed microstructures.

74. (Canceled)

75. The method of Claim 74, further comprising depositing a seed layer, substantially comprised of the same metal to be deposited to substantially fill the recessed microstructures, onto the surface of the workpiece prior to depositing the metal to substantially fill the recessed microstructures.

76-116. (Canceled)

117. A method of processing a microelectronic workpiece having a surface including a sub-micron recessed microstructure, comprising:

electroplating copper at an electroplating station to substantially fill the recessed microstructure and to deposit excess copper above the recessed microstructure;

thereafter, robotically transferring the workpiece from the electroplating station for at least one further process that includes thermal processing at a thermal processing station, the thermal processing comprising thermally treating the electroplated copper by establishing a temperature gradient through the electroplated copper having a maximum gradient temperature of about 60 degrees Celsius to about 100 degrees Celsius for no

longer than 15 minutes, thereby reducing resistivity of the copper and reducing voids which may be present in the copper.

118. The method of Claim 117 further comprising removing the excess copper after the thermal processing.

119. The method of Claim 118 wherein the excess copper is removed via chemical mechanical polishing.

120. The method of Claim 117 wherein the workpiece is thermally treated with a plurality of other workpieces in batch processing fashion.

121. The method of Claim 117 wherein the workpiece is thermally treated by flowing a temperature-controlled fluid over a surface of the workpiece.

122. The method of Claim 121 wherein the temperature-controlled fluid comprises a gas.

123. The method of Claim 121 wherein the temperature-controlled fluid comprises a cooling fluid.

124. The method of Claim 117 wherein the workpiece is thermally treated by radiant heating.

125. The method of Claim 117 wherein the workpiece is thermally treated by a hot plate.

126. The method of Claim 117 further comprising sensing a control temperature and controlling heating of the workpiece in response to the sensed temperature.

127. The method of Claim 126 wherein the control temperature is a temperature of a coolant fluid after the fluid is brought into thermal contact with the workpiece.

18 7
128. The method of Claim 117 wherein the temperature in the temperature gradient decreases in a direction outwardly from the filled recessed microstructure toward the excess copper.

19 7
129. The method of Claim 117 wherein the temperature gradient induces a stress gradient in the electroplated copper.

20
130. A method of treating a workpiece having a base having a surface, a dielectric layer carried on the surface of the base, and recessed sub-micron structures formed in the dielectric layer, comprising:

depositing a conductive seed layer exterior to the dielectric layer and in the recessed sub-micron structures;

contacting the seed layer with a copper-containing electroplating solution;

applying electroplating power to the seed layer to electrolytically deposit copper metal from the electroplating solution to substantially fill the recessed sub-micron structures and to deposit excess copper metal which extends beyond an exterior surface of the dielectric layer; then

subjecting the electroplated workpiece to an elevated temperature annealing process comprising establishing a temperature gradient in which the temperature decreases in a direction moving outwardly from the base toward the dielectric layer, the annealing process having a maximum gradient temperature which is no greater than about 250 degrees Celsius.

21 20
131. The method of Claim 130 wherein the maximum temperature of the annealing process is at or below about 100 degrees Celsius.

22 20
132. The method of Claim 130 wherein the maximum temperature of the annealing process is between about 60 degrees Celsius and about 100 degrees Celsius.

23 20
D1 133. The method of Claim 130 wherein the workpiece is subjected to the annealing process for no longer than 15 minutes.

24 20
134. The method of Claim 130 wherein the workpiece is subjected to the annealing process for less than one minute.

25 20
135. The method of Claim 130 further comprising depositing a barrier layer on the dielectric layer and in the sub-micron structures prior to depositing the seed layer.

26 20
136. The method of Claim 130 wherein the temperature gradient induces a stress gradient in the electroplated copper.

27 20
137. The method of Claim 130 wherein the electroplating power is initially applied at a first current for a predetermined first period of time, then applied at a higher second current for a predetermined second period of time.

28 20
138. The method of Claim 130 wherein the electroplating power is applied as a direct current.

29 20
139. The method of Claim 130 wherein the electroplating power is applied as a pulsed waveform at a frequency of between 1 and 1000 Hz.

30 29
140. The method of Claim 139 wherein the frequency of the pulsed waveform is between 5 and 20 Hz with a duty cycle of at least 50 percent.

31 20
141. The method of Claim 130 wherein the workpiece is spun while electroplating power is applied.

32 20
142. The method of Claim 130 further comprising removing the excess copper after the workpiece is subjected to the elevated temperature annealing process.

33
143. (Amended) A method of treating a microelectronic workpiece having a base having a surface including a sub-micron recessed microstructure, comprising:

D1 contacting the surface of the workpiece with a copper-containing electroplating solution;

applying electroplating power at a first power level for a first period of time, then applying electroplating power at a higher second power level for a time sufficient to substantially fill the recessed sub-micron structures with electroplated copper metal and to deposit excess copper metal above the sub-micron recessed microstructures; then

subjecting the electroplated workpiece to an elevated temperature annealing process comprising establishing a temperature gradient through the electroplated copper metal in which the temperature decreases in a direction moving outwardly from the base toward the workpiece surface, the annealing process having a maximum gradient temperature which is no greater than about 250 degrees Celsius.

34 33
144. The method of Claim 143 wherein the maximum temperature of the annealing process is at or below about 100 degrees Celsius.

35 33
145. The method of Claim 143 wherein the maximum temperature of the annealing process is between about 60 degrees Celsius and about 100 degrees Celsius.

36 33
146. The method of Claim 143 wherein the workpiece is subjected to the annealing process for no longer than 15 minutes.

37 33
147. The method of Claim 143 wherein the workpiece is subjected to the annealing process for less than one minute.

38 33
148. The method of Claim 143 further comprising depositing a barrier layer on the dielectric layer and in the sub-micron structures prior to depositing the seed layer.

39 33
149. The method of Claim 143 wherein the temperature gradient induces a stress gradient in the electroplated copper.

40 33
150. The method of Claim 143 wherein the seed layer is contacted with the electroplating solution for a predetermined dwell period before the electroplating power is applied.

41 33
151. The method of Claim 143 wherein the electroplating power is applied as a direct current.

42 33
152. The method of Claim 143 wherein the electroplating power is applied as a pulsed waveform at a frequency of between 1 and 1000 Hz.

43 42
153. (Amended) The method of Claim 152 wherein the frequency of the pulsed waveform is between 5 and 20 Hz with a duty cycle of at least 50 percent.

44 33
154. The method of Claim 143 wherein the workpiece is spun while electroplating power is applied.

45 33
155. The method of Claim 143 further comprising removing the excess copper after the workpiece is subjected to the elevated temperature annealing process.

46
156. (Amended) A method of treating a microelectronic workpiece having a base having a surface including a sub-micron recessed microstructure, comprising:

contacting the surface of the workpiece with a copper-containing electroplating solution;

applying electroplating power to the workpiece in a pulsed waveform having a frequency of between about 1 and 1000 Hz to substantially fill the recessed sub-micron structures with electroplated copper metal and to deposit excess copper metal above the sub-micron recessed microstructures; then

subjecting the electroplated workpiece to an elevated temperature annealing process comprising establishing a temperature gradient through the electroplated copper in which the temperature decreases in a direction moving outwardly from the base toward the surface of the workpiece, the temperature gradient having a maximum gradient temperature which is no greater than about 250 degrees Celsius.

47 46
157. The method of Claim 156 wherein the frequency of the pulsed waveform is between 5 and 20 Hz with a duty cycle of at least 50 percent.

D1

158. The method of Claim 156 wherein the maximum temperature of the annealing process is at or below about 100 degrees Celsius.

159. The method of Claim 156 wherein the maximum temperature of the annealing process is between about 60 degrees Celsius and about 100 degrees Celsius.

160. The method of Claim 156 wherein the workpiece is subjected to the annealing process for no longer than 15 minutes.

161. The method of Claim 156 wherein the workpiece is subjected to the annealing process for less than one minute.

162. The method of Claim 156 wherein a dielectric is carried on a surface of the base, the sub-micron recessed microstructures being formed in the dielectric layer, the method further comprising depositing a conductive seed layer exterior to the dielectric layer and in the sub-micron recessed microstructures.

163. The method of Claim 162 further comprising depositing a barrier layer on the dielectric layer and in the sub-micron structures prior to depositing the seed layer.

164. The method of Claim 162 wherein the electroplating power is applied to the seed layer.

165. The method of Claim 156 wherein the temperature gradient induces a stress gradient in the electroplated copper.

166. The method of Claim 156 wherein the seed layer is contacted with the electroplating solution for a predetermined dwell period before the electroplating power is applied.

167. The method of Claim 156 wherein the electroplating power is applied as a direct current.

58 46
168. The method of Claim 156 wherein the electroplating power is initially applied at a first current for a predetermined first period of time, then applied at a higher second current for a predetermined second period of time.

59 46
169. The method of Claim 156 wherein the workpiece is spun while electroplating power is applied.

60 46
170. The method of Claim 156 further comprising removing the excess copper after the workpiece is subjected to the elevated temperature annealing process.

61
171. A method of processing a microelectronic workpiece having a surface including a sub-micron recessed microstructure and a conductive seed layer in the sub-micron recessed microstructure, comprising:

electroplating copper at an electroplating station to substantially fill the recessed microstructure and to deposit excess copper which extends above the sub-micron recessed microstructure;

robotically transferring the workpiece from the electroplating station for further processing;

removing the excess copper as one of the further processes; and

thereafter, robotically transferring the workpiece to a thermal processing station to thermally treat the electroplated copper by establishing a temperature gradient in the electroplated copper having a maximum temperature of about 60 degrees Celsius to about 100 degrees Celsius for no longer than 15 minutes, thereby reducing resistivity of the copper and reducing voids which may be present in the copper.

62 61
172. The method of Claim 171 wherein the excess copper is removed via chemical mechanical polishing.

63 61
173. The method of Claim 171 wherein the microelectronic workpiece is thermally treated with a plurality of other semiconductor workpieces in batch processing fashion.

64 61
174. The method of Claim 171 wherein the microelectronic workpiece is thermally treated by flowing a temperature-controlled fluid over a surface of the workpiece.

175. The method of Claim 174 wherein the temperature-controlled fluid comprises a gas.

176. The method of Claim 174 wherein the temperature-controlled fluid comprises a cooling fluid.

177. The method of Claim 171 wherein the semiconductor workpiece is thermally treated by radiant heating.

178. The method of Claim 171 wherein the semiconductor workpiece is thermally treated by a hot plate.

179. The method of Claim 171 further comprising sensing a control temperature and controlling heating of the workpiece in response to the sensed temperature.

180. The method of Claim 179 wherein the control temperature is a temperature of a coolant fluid after the fluid is brought into thermal contact with the workpiece.

181. The method of Claim 171 wherein the temperature in the temperature gradient decreases in a direction outwardly from the filled recessed microstructure toward the excess copper.

182. The method of Claim 171 wherein the temperature gradient induces a stress gradient in the electroplated copper.

183-185. (Canceled)

186. (New) A method of treating a microelectronic workpiece having a base having a surface including a sub-micron recessed microstructure, comprising:

contacting the surface of the workpiece with a copper-containing electroplating solution;

applying electroplating power at a first power level for a first period of time, then applying electroplating power at a higher second power level for a time sufficient to

substantially fill the recessed sub-micron structures with electroplated copper metal and to deposit excess copper metal above the sub-micron recessed microstructures; then

subjecting the electroplated workpiece to an elevated temperature annealing process for a time sufficient to reduce resistivity of the electroplated copper metal.

187. (New) The method of Claim 186 wherein a maximum temperature of the annealing process is at or below about 250 degrees Celsius.

188. (New) The method of Claim 186 wherein a maximum temperature of the annealing process is at or below about 100 degrees Celsius.

189. (New) The method of Claim 186 wherein a maximum temperature of the annealing process is between about 60 degrees Celsius and about 100 degrees Celsius.

190. (New) The method of Claim 186 wherein the workpiece is subjected to the annealing process for no longer than 15 minutes.

191. (New) The method of Claim 186 wherein the workpiece is subjected to the annealing process for less than one minute.

192. (New) The method of Claim 186 further comprising depositing a barrier layer on the dielectric layer and in the sub-micron structures prior to depositing the seed layer.

193. (New) The method of Claim 186 wherein the annealing process comprises establishing a temperature gradient through the electroplated copper metal in which the temperature decreases in a direction moving outwardly from the base toward the workpiece surface, the temperature gradient inducing a stress gradient in the electroplated copper.

D1

194. (New) The method of Claim 186 wherein the seed layer is contacted with the electroplating solution for a predetermined dwell period before the electroplating power is applied.

82 73
195. (New) The method of Claim 186 wherein the electroplating power is applied as a direct current.

83 73
196. (New) The method of Claim 186 wherein the electroplating power is applied as a pulsed waveform at a frequency of between 1 and 1000 Hz.

84 73
197. (New) The method of Claim 196 wherein the frequency of the pulsed waveform is between 5 and 20 Hz with a duty cycle of at least 50 percent.

85 73
198. (New) The method of Claim 186 wherein the workpiece is spun while electroplating power is applied.

86 73
199. (New) The method of Claim 186 further comprising removing the excess copper after the workpiece is subjected to the elevated temperature annealing process.

87
200. (New) A method of treating a microelectronic workpiece having a base having a surface including a sub-micron recessed microstructure, comprising:

contacting the surface of the workpiece with a copper-containing electroplating solution;

applying electroplating power to the workpiece in a pulsed waveform having a frequency of between about 1 and 1000 Hz to substantially fill the recessed sub-micron structures with electroplated copper metal and to deposit excess copper metal above the sub-micron recessed microstructures; then

subjecting the electroplated workpiece to an elevated temperature annealing process for a time sufficient to reduce resistivity of the electroplated copper metal.

88 87
201. (New) The method of Claim 200 wherein the frequency of the pulsed waveform is between 5 and 20 Hz with a duty cycle of at least 50 percent.

89 87
202. (New) The method of Claim 200 wherein a maximum temperature of the annealing process is at or below about 250 degrees Celsius.

D1

203. (New)The method of Claim 200 wherein a maximum temperature of the annealing process is at or below about 100 degrees Celsius.

204. (New) The method of Claim 200 wherein a maximum temperature of the annealing process is between about 60 degrees Celsius and about 100 degrees Celsius.

205. (New) The method of Claim 200 wherein the workpiece is subjected to the annealing process for no longer than 15 minutes.

206. (New) The method of Claim 200 wherein the workpiece is subjected to the annealing process for less than one minute.

207. (New) The method of Claim 200 wherein a dielectric is carried on a surface of the base, the sub-micron recessed microstructures being formed in the dielectric layer, the method further comprising depositing a conductive seed layer exterior to the dielectric layer and in the sub-micron recessed microstructures.

208. (New) The method of Claim 207 further comprising depositing a barrier layer on the dielectric layer and in the sub-micron structures prior to depositing the seed layer.

209. (New) The method of Claim 207 wherein the electroplating power is applied to the seed layer.

210. (New) The method of Claim 200 wherein the annealing process comprises establishing a temperature gradient through the electroplated copper metal in which the temperature decreases in a direction moving outwardly from the base toward the workpiece surface, the temperature gradient inducing a stress gradient in the electroplated copper.

211. (New) The method of Claim 200 wherein the seed layer is contacted with the electroplating solution for a predetermined dwell period before the electroplating power is applied.